

ISSUE CLASSIFICATION	
Class	Subclass

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**PATENT NUMBER**

**U.S. UTILITY Patent Application**

<p>O.I.P.E.</p> <p>SCANNED <u>TRI</u> Q.A. <u>Am</u></p>	<p>PATENT DATE</p>
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JC-1118

APPLICATION NO. 09/943196	CONT/PRIOR	CLASS 438	SUBCLASS 692	ART UNIT 1765	EXAMINER Ym(z-Eronini)
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Charles May

Arrangement and method for fabricating a semiconductor wafer

PTO-2040  
12/89**ISSUING CLASSIFICATION**

<b>ISSUING CLASSIFICATION</b>																
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>												
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>											
<b>INTERNATIONAL CLASSIFICATION</b>																

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
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<input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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